

Abstract

A semiconductor integrated circuit is provided which comprises a serial/parallel conversion circuit for converting serial data which is input together with a clock signal into parallel data; a memory for storing the parallel data converted by the serial/parallel conversion circuit; and a write pulse producing circuit for producing a pulse to be used to set the period of time for writing data into the memory. The semiconductor integrated circuit converts serial data into parallel data, stores the parallel data, and stably operates even when the cycle of data writing/reading is shortened.